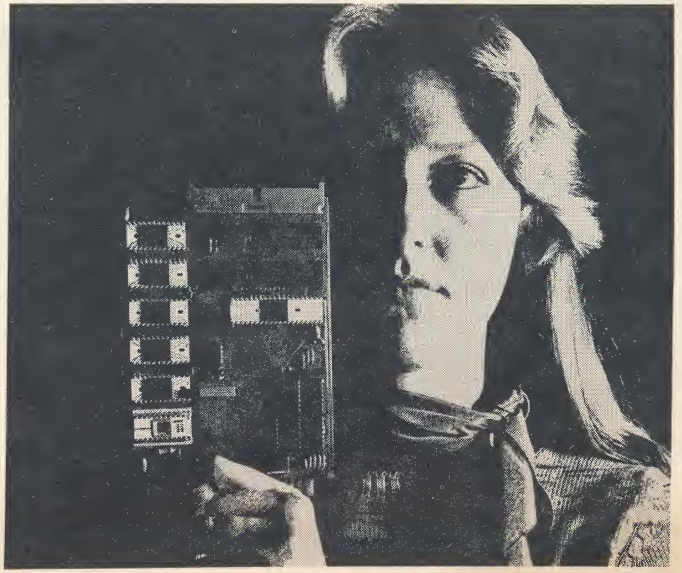
Microprocessor Short Course with Free Take Home Microcomputer

Jan. 25, 26, 27	Dayton, OH
Feb. 1, 2, 3	St. Petersburg Beach, FL
Feb. 9, 10, 11	Los Angeles, CA
Feb. 14, 15, 16	Palo Alto, CA
Feb. 22, 23, 24	Philadelphia, PA
Mar. 1, 2, 3	Washington, DC
Mar. 8, 9, 10	Boston, MA
Mar. 15, 16, 17	Chicago, IL
Mar. 22, 23, 24	Huntsville, AL

Learn microprocessors first hand from the original hands on people.



WINTEK

WINTEK Corp.
902 N. 9th St.
Lafayette, IN 47904

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(parallel I/O) and is fully expandable to 65K bytes RAM, ROM, I/O. It is bus compatible with the other WINCE modules (ROM, RAM, EROM programmer, A/D converter). The ROM contains FANTOM II, a monitor/debug program that allows single step execution of user programs, insertion and deletion of breakpoints.

Call Alice Anderson (317) 742-6802 or write WINTEK Corp., 902 N. 9th St., Lafayette, IN 47904.

Hands on Workshop

In the workshops the attendees work in 2-person groups. Each has an interactive CRT terminal and microcomputer. The experiments start with loading and editing a simple program and ends with writing, debugging, and executing a program involving I/O and interrupts.

Inquire about our vendor-independent training programs in your plant. We have presented in-house courses at USAF/WPAFB/AFAL, USN/NESEC, Perkin Elmer, NASA/WS, NASA/GSFC, DCA, etc.

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Registration Form Please enroll the following person(s) for the course indicated. Registration fee(s) enclosed.

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- ☐ Mar. 22, 23, 24 Huntsville, AL

Name(s) _____

Address(es) _____

_____

Make check payable to WINTEK Corp. and mail with this form to:
WINTEK Corp., 902 N. 9th St., Lafayette, IN 47904

Please send information on:

- ☐ In house short courses
- ☐ 6800 μP hardware/software
- ☐ $875 CRT terminal
- ☐ Consulting services
- ☐ Custom hardware/software
- ☐ Future short courses

PLEASE POST

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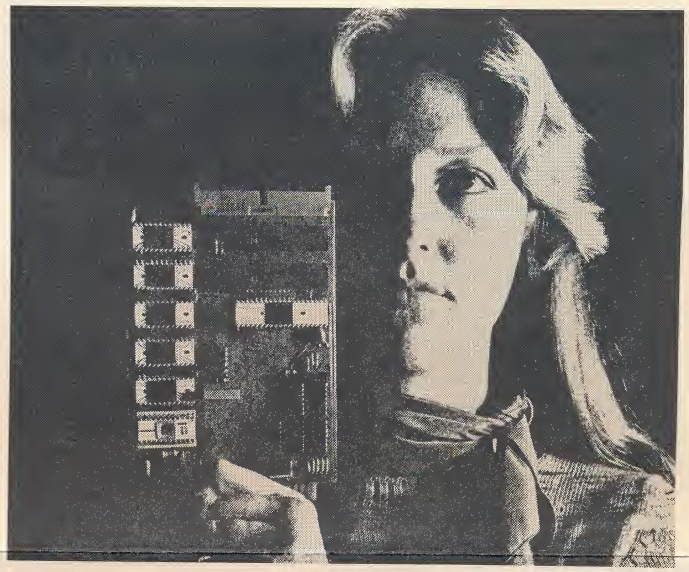

Lecture Topics

μP Hardware —	MPU, RAM, ROM, EROM, I/O interfacing, design techniques
μP Software —	machine language, hexadecimal, assembly language, programming techniques
Economics —	hardware/software tradeoffs, assembly/high level language tradeoffs
also —	processor evaluation and selection, architectures and instruction sets
also —	logic/PLA/μP tradeoffs

Take Home Microcomputer

Each attendee will receive a microcomputer to use at the course and then take home. It is the student configuration of our standard WINCE control module shown in the picture. The module includes a 6800 MPU, clock, 1K byte ROM, 256 byte RAM, ACIA (UART serial I/O), PIA (parallel I/O) and is fully expandable to 65K bytes RAM, ROM, I/O. It is bus compatible with the other WINCE modules (ROM, RAM, EROM programmer, A/D converter). The ROM contains FANTOM II, a monitor/debug program that allows single step execution of user programs, insertion and deletion of breakpoints.

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Faculty

Paul Wintz, Ph.D. — Professor of Electrical Engineering, Purdue University, and president of WINTEK Corp., 10 years experience in teaching, research, and consulting. Author of 3 books, 70 papers, and 2 patents.

Jim Wilson, MSEE — Vice President for Engineering, WINTEK Corp. He designed the WINCE micro-modules, BRB video terminal and a number of application systems, floppy disk interface, etc. Former instructor at Purdue University.

Fee

$495 includes tuition, notes, all course materials, and take home microcomputer. Discount for multiple enrollments from the same firm.

Information

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